

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0035		
				Application No.: 10/675,543		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Jerrell Hein and Axel Thomsen		
(Use several sheets if necessary)				Filing Date: September 30, 2003		
				Group Art Unit: 2817		
				Date Submitted: October 4, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
<i>RL</i>	AA	3,676,801	Jul. 11, 1972	Musa		
<i>RL</i>	AB	3,719,838	Mar. 6, 1973	Peduto et al.		
<i>RL</i>	AC	3,887,941	Jun. 3, 1975	Dann et al.		
<i>RL</i>	AD	4,038,612	Jul. 26, 1977	Borofka et al.		
<i>RL</i>	AE	4,135,158	Jan. 16, 1979	Parmet		
<i>RL</i>	AF	4,380,745	Apr. 19, 1983	Barlow et al.		
<i>RL</i>	AG	4,446,446	May 1, 1984	Fowks		
<i>RL</i>	AH	4,485,818	Dec. 4, 1984	Leckrone et al.		
<i>RL</i>	AI	4,580,107	Apr. 1, 1986	Caldwell et al.		
<i>RL</i>	AJ	4,598,257	Jul. 1, 1986	Southard		
<i>RL</i>	AK	4,683,445	Jul. 28, 1987	Erickson		
Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
<i>RL</i>	AL	0 239 412 A2	Sep. 30, 1987	Europe		
<i>RL</i>	AM	0 393 975 A1	Oct. 24, 1990	Europe		
<i>RL</i>	AN	0 402 736 A2	Dec. 19, 1990	Europe		
<i>RL</i>	AO	0 601 780 A2	Jun. 15, 1994	Europe		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>RL</i>	AP	Cypress Semiconductor Corporation, "Cypress 1 PLL In-System Programmable Clock Generator with Individual 16K EEPROM," December 14, 2002, 18 pages.				
<i>RL</i>	AQ	Encinas, J. B., "Phase Locked Loops," Chapman & Hall, 1993, pages 144-158.				
<i>RL</i>	AR	Foord, A., "Voltage Controlled Oscillator," Radio & Electronics Constructor, May 1975, pp. 590-595. 592-4				
<i>RL</i>	AS	Fox Electronics, "1995-1996 Frequency Control Products Catalog," Fort Meyers, Florida, 1995, 72 pages.				
Examiner <i>Joseph Chang</i>		Date Considered <i>3/11/05</i>				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0035		
				Application No.: 10/675,543		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Jerrell Hein and Axel Thomsen		
(Use several sheets if necessary)				Filing Date: September 30, 2003		
				Group Art Unit: 2817		
				Date Submitted: October 4, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
<i>RL</i>	AT	4,689,581	Aug. 25, 1987	Talbot		
<i>RL</i>	AU	4,827,226	May 2, 1989	Connell		
<i>RL</i>	AV	4,847,888	Jul. 11, 1989	Cox et al.		
<i>RL</i>	AW	4,859,968	Aug. 22, 1989	Gershon		
<i>RL</i>	AX	4,891,825	Jan. 2, 1990	Hansen		
<i>RL</i>	AY	4,931,748	Jun. 5, 1990	McDermott et al.		
<i>RL</i>	AZ	4,940,950	Jul. 10, 1990	Helfrick		
<i>RL</i>	AAA	4,967,165	Oct. 30, 1990	Lee et al.		
<i>RL</i>	ABB	4,969,210	Nov. 6, 1990	Hansen et al.		
<i>RL</i>	ACC	4,996,608	Feb. 26, 1991	Widney		
<i>RL</i>	ADD	5,041,800	Aug. 20, 1991	Long et al.		
Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
<i>RL</i>	AEE	WO 85/ 02966	Jul. 4, 1985	PCT		
<i>RL</i>	AFF	02-291161 (English abstract)	Nov. 30, 1990	Japan		X
<i>RL</i>	AGG	03-297223 (English abstract)	Dec. 27, 1991	Japan		X
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>RL</i>	AHH	Fox Electronics, "F3000 / HCMOS/ITL Tri-State Enable/Disable Oscillator," 1 page (from file history of U.S. Patent No. 5,952,890).				
<i>RL</i>	AIL	Fox Electronics, "F6151 / Tri-State HCMOS Dual Programmable Clock Oscillator," 4 pages (from file history of U.S. Patent No. 5,952,890).				
<i>RL</i>	AJJ	Fox Electronics, "F6053A / Tri-State HCMOS Programmable Clock Oscillator," 4 pages (from file history of U.S. Patent No. 5,952,890).				
<i>RL</i>	AKK	Fox Electronics, "F6233 Quick Delivery Oscillator," 1 page (from file history of U.S. Patent No. 5,952,890).				
Examiner <i>Joseph Chang</i>		Date Considered 3/11/05				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						

U.S. Department of Commerce, Patent and Trademark Office			Attorney Docket No.: 026-0035	
			Application No.: 10/675,543	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Applicant(s): Jerrell Hein and Axel Thomsen	
(Use several sheets if necessary)			Filing Date: September 30, 2003	
			Group Art Unit: 2817	
			Date Submitted: October 4, 2004	
U.S. Patent Documents				
*Examiner Initial		Document Number	Date	Name
Jc	ALL	5,081,431	Jan. 14, 1992	Kubo et al.
Jc	AMM	5,099,153	Mar. 24, 1992	Adams
Jc	ANN	5,144,254	Sep. 1, 1992	Wilke
Jc	AQQ	5,179,359	Jan. 12, 1993	McLeod
Jc	APP	5,180,992	Jan. 19, 1993	Akiyama et al.
Jc	AQQ	5,233,351	Aug. 3, 1993	Gregory et al.
Jc	ARR	5,254,958	Oct. 19, 1993	Flach et al.
Jc	ASS	5,259,007	Nov. 2, 1993	Yamamoto
Jc	ATT	5,263,197	Nov. 16, 1993	Manjo et al.
Jc	AUU	5,266,908	Nov. 30, 1993	Koulopoulos et al.
Jc	AVV	5,319,324	Jun. 7, 1994	Satoh et al.
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)				
Jc	AWW	Gardner, Floyd M., "Phaselock Techniques," Second Edition, John Wiley & Sons, 1979, pages 2-5.		
Jc	AXX	Gaskell, Tom, "Semiconductor Circuits Programmable Crystal Oscillator (PX0-600)," <u>Practical Electronics</u> , July 1983, pages 40-43.		
Jc	AYY	Giles, T. G., "A Universal Frequency Synthesizer IC," Philips Telecommunication Review, Vol. 37, No. 3, August 1979, pages 177-181.		
Jc	AZZ	Goto, Junichi, et al., "A Programmable Clock Generator With 50 to 350 MHz Lock Range For Video Signal Processors," IEEE 1993 Custom Integrated Circuits Conference, pages 4.4.1 - 4.4.2.		
Jc	AAAA	"IC Designs Introduces Trio of Parts Aimed at Replacing Crystal Oscillators in Personal Computer Graphics Board Designs," News Release, May 9, 1990, 1 page.		
Jc	ABBB	Hybrids International, Ltd., "Hybrids International Product Summary," March 1993, Olathe, Kansas, 7 <sup>1</sup> / <sub>5</sub> pages.		
Jc	ACCC	IC Designs, "Satellite Oscillator," Model No. ICD2031A Preliminary Data Sheet, V2.0, May 1991, pages 1-8.		
Jc	ADDD	IC Designs, "Frequency Multiplier," Model No. ICD2032 Preliminary Data Sheet, V2.0, May 1991, pages 1-7.		
Jc	AEEE	IC Designs, "Dual VGA Clock Generator," Model No. ICD2042A, prior to 1995, 1 page.		
Examiner	Joseph Chang		Date Considered 3/11/05	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.				

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0035
				Application No.: 10/675,543
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Jerrell Hein and Axel Thomsen
(Use several sheets if necessary)				Filing Date: September 30, 2003
				Group Art Unit: 2817
				Date Submitted: October 4, 2004
U.S. Patent Documents				
*Examiner Initial		Document Number	Date	Name
JH	AFFF	5,349,544	Sep. 20, 1994	Wright et al.
JH	AGGG	5,416,443	May 16, 1995	Cranford, Jr. et al.
JH	AHHH	5,416,446	May 16, 1995	Holler, Jr. et al.
JH	AIII	5,428,319	Jun. 27, 1995	Marvin et al.
JH	AJJJ	5,448,204	Sep. 5, 1995	Gardner
JH	AKKK	5,451,912	Sep. 19, 1995	Torode
JH	ALLL	5,471,661	Nov. 28, 1995	Atkinson
JH	AMMM	5,473,289	Dec. 5, 1995	Ishizaki et al.
JH	ANNN	5,473,758	Dec. 5, 1995	Allen et al.
JH	AOOO	5,952,890	Sep. 14, 1999	Fallisgaard et al.
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)				
JH	APPP	IC Designs, "Dual Programmable Clock Oscillator," Model No. ICD2051, V2.0, May 1991, pages 1-12.		
JH	AQQQ	IC Designs, "Programmable Clock Oscillator," Model No. ICD2053A Programmable Products, V1.0, prior to 1995, pages 14 pages.		
JH	ARRR	IC Designs, "Graphics Frequency Synthesizers," Model No. ICD2061A, prior to 1995, 1 page.		
JH	ASSS	IC Designs, "Dual Programmable ECL/TTL Clock Generator," Model No. ICD2062B, prior to 1995, 1 page.		
JH	ATTT	IC Designs, "Programmable Graphics Clock Generator," Model No. ICD 2063, prior to 1995, 1 page.		
JH	AUUU	IC Designs, Model No. ICD 6233, Specifications, February 11, 1994, 32 pages		
JH	AVVV	IC Designs, "Application Note: Power Feed and Board Layout Issues," May 1991, 3 pages.		
JH	AWWW	Lovelock, Peter A., "Build This Programmable Crystal-Controlled Pulse Generator," Radio Electronics, June 1990, pages 47-50.		
JH	AXXX	MF Electronics Corp., "Crystal Controlled Oscillators 20 to 220 MHz Programmable ECL," prior to 1995, pages 9, 11, 18-20.		
JH	AYYY	Mielke, Erhard, "XP-002061415 Programmierbarer 50-MHz-Mutertaktimpulsgenerator," 2421 Radio Fernsehen Elektronik 39 (1990) No. 9, Berlin, 4 pages		
Examiner	Joseph Chang		Date Considered	3/11/05
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.				

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0035		
				Application No.: 10/675,543		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Jerrell Hein and Axel Thomsen		
(Use several sheets if necessary)				Filing Date: September 30, 2003		
				Group Art Unit: 2817		
				Date Submitted: October 4, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
<i>Jr</i>	AZZZ	6,008,703	Dec. 28, 1999	Perrott et al.		
<i>Jr</i>	AAAAA	6,456,164	Sep. 24, 2002	Fan		
<i>Jr</i>	ABBBB	6,570,453	May 27, 2003	Su et al.		
<i>Jr</i>	ACCCC	5,604,468	Feb. 18, 1997	Gillig		
Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>Jr</i>	ADDDD	Epson, "Programmable Multi-Output Crystal Oscillator MG-5010," Mar. 7, 1993, 3 pages.				
<i>Jr</i>	AEEEE	Wenzel Associates, "Custom Oscillator Configurations," Austin, Texas, 1 page (from file history of U.S. Patent No. 5,952,890)				
<i>Jr</i>	AFFFF	M-Tron, "MT1135 Series Dual Baud Rate Generators," 1987, 3 pages.				
<i>Jr</i>	AGGGG	M-Tron, "MV Series VCXO Oscillators," prior to 1995, 1 page				
<i>Jr</i>	AHHHH	National Semiconductor, "PLL™ Low Power Frequency Synthesizer for RF Personal Communications," Advance Information, January 1998, 18 pages.				
<i>Jr</i>	AIIII	Soderquist, Donn, "Digitally Programmed Oscillator is Suitable for $\mu$ P Control," Precision Monolithics Inc., June 21, 1997, pp. 102, 104.				
<i>Jr</i>	AJJJJ	Ooms, William J., "Improved Frequency Synthesizer," 8182 Motorola Technical Disclosure Bulletin, October 1986, Schaumbourg, ILL, 3 pages.				
<i>Jr</i>	AKKKK	Vectron Laboratories, "Sonet Clock Recovery Module 622.08 MHz," Model No. SCRM-622, 1994, 4 pages.				
Examiner <i>Joseph Chang</i>		Date Considered 5/11/05				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0035		
				Application No.: 10/675,543		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Applicant(s): Jerrell Hein and Axel Thomsen		
(Use several sheets if necessary)				Filing Date: September 30, 2003		
				Group Art Unit: 2817		
				Date Submitted: October 4, 2004		
<b>U.S. Patent Documents</b>						
*Examiner Initial		Document Number	Date	Name		
JC	ALLL	5,790,614	Aug. 04, 1998	Powell		
JC	AMMM	6,404,246	Jun. 11, 2002	Estakhri et al.		
	ANNN	<del>10/675,529</del>	<del>Sep. 30, 2003</del>	<del>Hein</del>		
<b>Foreign Patent Documents</b>						
					<b>Translation</b>	
		Document	Date	Country	Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
JC	A0000	De Muer, Bram, and Steyaert, Michel S. J., "A CMOS Monolithic $\Delta\Sigma$ -Controlled Fractional-N Frequency Synthesizer for DCS-1800," IEEE Journal of Solid-State Circuits, Vol. 37, No. 7, July 2002, pp. 835-844.				
JC	APPPP	Riley, Tom A. D., et al., "Delta-Sigma Modulation in Fractional-N Frequency Synthesis," IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, pp. 553-559.				
JC	AQQQQ	Parmarti, Sudhakar, et al., "A Wideband 2.4-GHz Delta-Sigma Fractional-N PLL With 1-Mb/s In-Loop Modulation," IEEE Journal of Solid-State Circuits, Vol. 39, No. 1, January 2004, pp. 49-62.				
Examiner <i>Joseph Chang</i>		Date Considered 3/11/05				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						

Not a publication